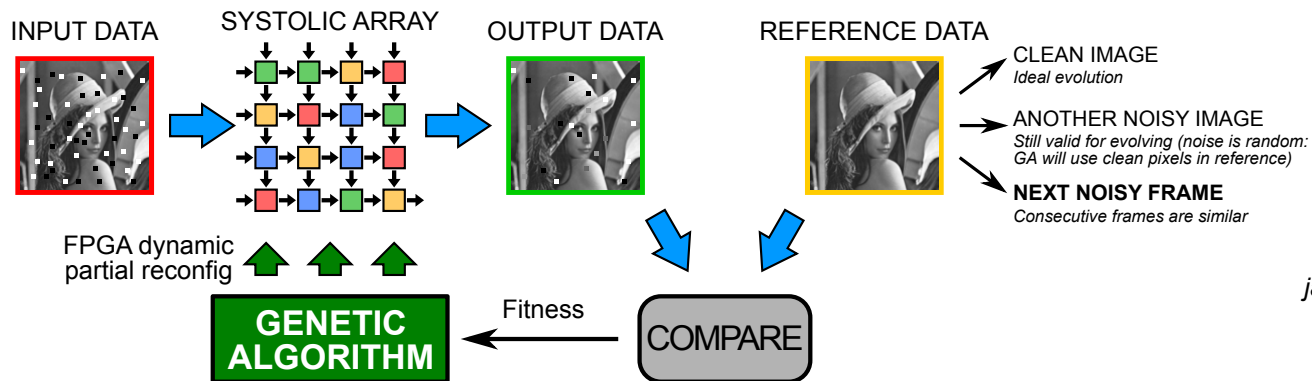


Increased Fault Tolerance in Evolvable Hardware through Automatic Upscaling

Javier Mora; Ángel Gallego; Andrés Otero; Eduardo de la Torre; Teresa Riesgo

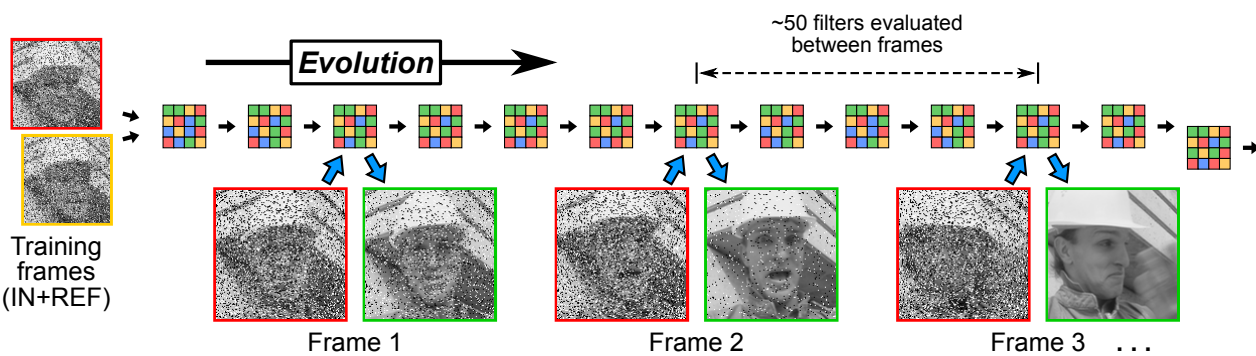
EVOLVABLE HARDWARE



Javier Mora

javier.morad@upm.es

REAL TIME EVOLUTION

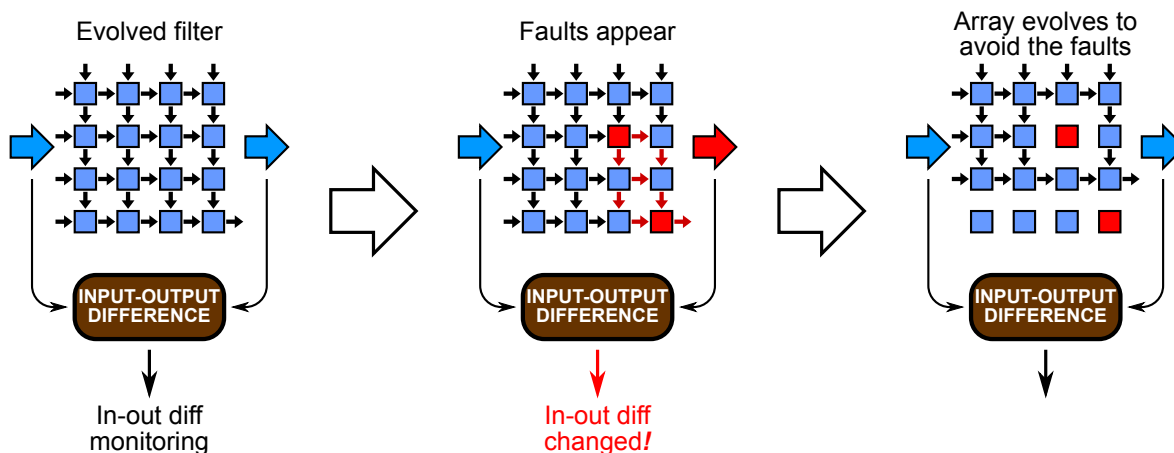


CEIUPM | Centro de Electrónica Industrial



POLITÉCNICA

SELF-HEALING



Sponsors:



AUTO-SCALING

